

Si Photo-transistor Chip – TKCS16PTS

1. Scope

- The specification applies to NPN silicon photo-transistor chip.
- Type : TKCS16PTS.

2. Structure

- NPN planar type.
- Electrode N (collector) side : Gold.
P (base) side : Aluminum.
N (emitter) side : Aluminum.

3. Size

- Chip size : $14.57\text{mil} \times 14.57\text{mil} \pm 1.5\text{mil}$ ($0.37\text{mm} \times 0.37\text{mm} \pm 0.038\text{mm}$)
- Thickness : $7\text{mil} \pm 1\text{mil}$ ($0.175\text{mm} \pm 0.025\text{mm}$)
- Active area : $11.2\text{mil} \times 11.2\text{mil} \pm 0.5\text{mil}$ ($0.286\text{mm} \times 0.286\text{mm} \pm 0.013\text{mm}$)
- Base pad size : diameter $3.15\text{mil} \pm 0.4\text{mil}$ ($0.08\text{mm} \pm 0.01\text{mm}$)
- Emitter pad size : $5.43\text{mil} \times 5.43\text{mil} \pm 0.4\text{mil}$ ($0.138\text{mm} \times 0.0138\text{mm} \pm 0.01\text{mm}$)
- Pattern drawing : per fig. 1

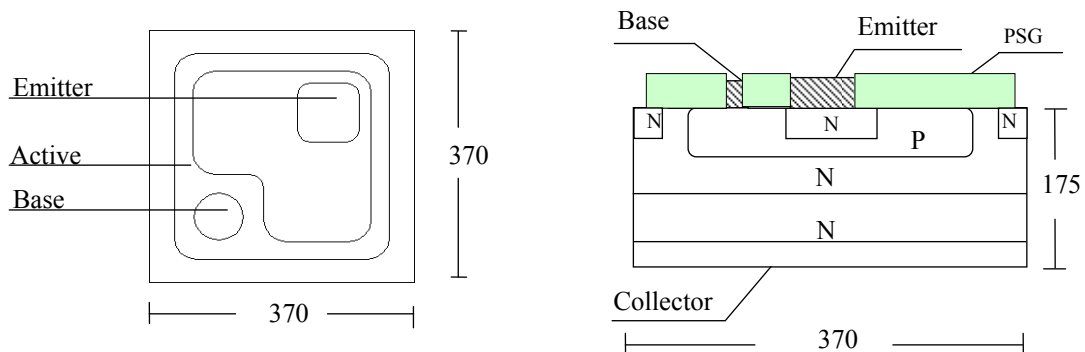
4. Electro-Optical Characteristics

($T_a = +25^\circ\text{C}$)

Parameter	Symbol	Condition	Min.	Typ.	Max	Unit
Collector-emitter Breakdown Voltage	BV_{CEO}	$I_{CEO}=100\mu\text{A}$	85			V
Emitter-collector Breakdown Voltage	BV_{ECO}	$I_{ECO}=10\mu\text{A}$ *Note1	8.2			V
Collector-base Breakdown Voltage	BV_{CBO}	$I_{CBO}=100\mu\text{A}$	85			V
Collector Dark Current	I_{CEO}	$V_{CE}=20\text{V}$			30	nA
Collector Dark Current	I_{CEO}	$V_{CE}=70\text{V}$			150	nA
Collector-Emitter Saturation Voltage	$V_{CE}(\text{Sat})$	$I_C=2\text{mA}$, $I_B=100\mu\text{A}$			0.3	V
Peak Sensing Wavelength	λ_P			880		nm
DC Current Amplification Factor	h_{FE}	$V_{CE}=5\text{V}$, $I_C=2\text{mA}$ Rank A1	200		400	
		Rank A	400		600	
		Rank B	500		750	
		Rank C	600		900	
		Rank D	800		1200	
		Rank E	1000		1500	
		Rank F	1200		1800	
		Rank H	1500		2300	

* Note 1 : $BV_{ECO} > 8.2$ 限 BIN (A1, A, B, C, D, E, F, H)

* Note 2 : $T_j > 140^\circ\text{C}$



Unit : mil

fig. 1

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